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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.		
10/771,526	02/04/2004	Howard L. Levy	004-30053	2891		
22120 7.	590 09/21/2005		EXAM	EXAMINER		
ZAGORIN O	BRIEN GRAHAM LLP	LAM, DAVID				
7600B N. CAP SUITE 350	ITAL OF TEXAS HWY.	ART UNIT	PAPER NUMBER			
AUSTIN, TX	78731		2827			
			DATE MAIL ED: 00/01/200			

Please find below and/or attached an Office communication concerning this application or proceeding.

		Applicati	on No.	Applicant(s)				
			10/771,526 LEVY ET AL.					
Office Action Summary		Examine	r	Art Unit				
		David La	n	2827				
The MAIL Period for Reply	ING DATE of this communi	cation appears on th	e cover sheet with the c	correspondence ac	ldress			
THE MAILING D  - Extensions of time m after SIX (6) MONTH  - If the period for reply - If NO period for reply - Failure to reply within Any reply received by	STATUTORY PERIOD FO ATE OF THIS COMMUNI ay be available under the provisions S from the mailing date of this comm specified above is less than thirty (30 is specified above, the maximum state the set or extended period for reply by the Office later than three months a djustment. See 37 CFR 1.704(b).	CATION. of 37 CFR 1.136(a). In no exunication. or days, a reply within the statutory period will apply and vill, by statute, cause the app	vent, however, may a reply be tir tutory minimum of thirty (30) day vill expire SIX (6) MONTHS from plication to become ABANDONE	nely filed vs will be considered time the mailing date of this of (35 U.S.C. § 133).	ly. communication.			
Status								
1) Responsiv	e to communication(s) file	d on <u>28 June 2005</u> .						
2a) ☐ This action	is FINAL. 2	b) This action is r	non-final.					
•	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.							
Disposition of Clair	ms							
4a) Of the a 5)⊠ Claim(s) <u>1</u> 6)□ Claim(s) _ 7)□ Claim(s) _	<ul> <li>Claim(s) 1-49 is/are pending in the application.</li> <li>4a) Of the above claim(s) is/are withdrawn from consideration.</li> <li>Claim(s) 1-49 is/are allowed.</li> <li>Claim(s) is/are rejected.</li> <li>Claim(s) is/are objected to.</li> </ul>							
Application Papers								
10)⊠ The drawin Applicant m Replaceme	cation is objected to by the g(s) filed on <u>04 February 2</u> ay not request that any object that drawing sheet(s) including reduction is objected to	$2004$ is/are: a) $\boxtimes$ action to the drawing(s) the correction is requi	be held in abeyance. Se red if the drawing(s) is ob	e 37 CFR 1.85(a). ejected to. See 37 C	FR 1.121(d).			
Priority under 35 U.	S.C. § 119							
12) Acknowled a) All b) Cert 2. Cert 3. Cop	gment is made of a claim of Some * c) None of: ified copies of the priority ified copies of the priority ies of the certified copies of ication from the Internation ched detailed Office action	documents have been documents have been been the priority documents Bureau (PCT Ru	en received. en received in Applicat ents have been receive le 17.2(a)).	ion No ed in this National	Stage			
Address of A								
Attachment(s)  1) ⊠ Notice of Reference	es Cited (PTO-892)		4) Interview Summary	(PTO-413)				
2) Notice of Draftsper	son's Patent Drawing Review (P sure Statement(s) (PTO-1449 or		Paper No(s)/Mail D 5) Notice of Informal F 6) Other:	ate	O-152)			

### **DETAILED ACTION**

This application is in condition for allowance except for the following formal matters:

### IN THE SPECIFICATION:

- On page 8, line 14, "connectors 420 and 426" should be change to -- connectors 422 and 426 -- in order to be consistent with Figure 4 of the drawing.

## Allowable Subject Matter

Claims 1-49 are allowable over the prior art of record because none of the prior art whether taken singularly or in combination, especially when these limitations are considered within the specific combination claimed, to teach: a system comprising: a control circuit, among others as claimed in independent claim 16, for generating an input signal for the first of the serially-connected word line driver initialization circuits when a power supply operably coupled to the memory circuit first achieves a first predetermined voltage, and a plurality of data circuits for conveying a data pattern on to the bit line of the memory array during the pulsing of the word line groups; means for asynchronously pulsing successive word line groups of one or more individual word lines of the memory array while conveying a data pattern onto bit lines of the memory array, and among others as claimed in independent claim 44. Method for initializing a memory cell comprising step of asynchronously pulsing successive word line groups of one or more individual word lines of the memory array while conveying a data pattern onto bit lines of the memory array, and among other as claimed in independent claim 1.

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#### Conclusion

Prosecution on the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

A shortened statutory period for reply to this action is set to expire **TWO MONTHS** from the mailing date of this letter.

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

- Oh (6,011,746) discloses a hierarchical word line driving structure uses a shared inverter circuit, which allows for lower power consumption and a pulsed control signal to ensure accurate memory retrieval.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to David Lam whose telephone number is 571-272-1782. The examiner can normally be reached on 6:00-4:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Hoai Ho can be reached on 571-272-1777. The fax phone numbers for the organization where this application or proceeding is assigned is (571) 272-8300.

Information regarding the status of an application may be obtained from the Patent

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DAVID LAM PRIMARY EXAMINER

D. Lam

September 19, 2005